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S/N 10/533489

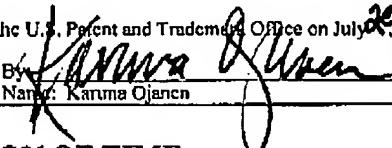
PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant:	TSUKADA et al.	Examiner:	INGHAM, John C.
Serial No.:	10/533489	Group Art Unit:	2814
Filed:	April 28, 2005	Docket No.:	10921.0313USWO
Title:	CHIP RESISTOR, PROCESS FOR PRODUCING THE SAME, AND FRAME FOR USE THEREIN		

CERTIFICATE UNDER 37 CFR 1.6(d):

I hereby certify that this paper is being transmitted by facsimile to the U.S. Patent and Trademark Office on July 20, 2007.

By: 
Name: Kanina Ojanen

PETITION FOR EXTENSION OF TIME

Mail Stop: AMENDMENT
Commissioner for Patents
P.O. Box 1450
Alexandria, Virginia 22313-1450

Dear Sir:

In accordance with the provisions of 37 C.F.R. §1.136(a), it is respectfully requested that a 1-month extension of time be granted in which to respond to the outstanding Office Action mailed March 21, 2007, said period of response being extended from June 21, 2007 to July 21, 2007.

Please charge Deposit Account No. 50-3478 in the amount of \$120.00 to cover the required extension fee for a large entity.

Respectfully submitted,



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By: 
Douglas P. Mueller
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DPM/lS

Dated: July 20, 2007

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